

Specification Amendments Under 37 C.F.R. § 1.121(b)(1)(iii)

Replace the paragraph at page 3, line 28 with the below paragraph marked up by way of bracketing and underlining to show the changes relative to the previous version of the paragraph.

Figure 1 shows the rotation positions for a wafer during array preparation. The position notations are used in the rotation prompts on the computer screen. By using these notations the input paddle of the MOS can be labeled A, B, C and D at the sections where the wafer label is supposed to be facing. That way when a certain position is prompted the operator can match the positioning of the wafer label with the letter on the MOS input paddle.